

Lead Test Engineer

Full-Time

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Company Overview

Chiplytics streamlines access to mission-critical electronic components through a scalable testing platform and growing network of supply chain partners. Mature nodes power nearly every electrical system, yet dependence on overseas manufacturing and complex distribution channels introduce significant risk to critical supply chains and national security. The Chiplytics Certified Authentic program will mitigate these risks by delivering secure sourcing, procurement, and testing for high-reliability electronics.

Job Description

We're hiring a **Lead Test Engineer** to lead all electronic component testing that flows through the Chiplytics Certified Authentic program. Reporting to the VP of Quality Assurance, you'll translate requirements into executable test plans, lead hands-on evaluation across AS6171 methods, train and mentor lab technicians, and help stand up our component testing lab infrastructure and workflows. You'll work closely with Product and Engineering to define requirements and deploy Chiplytics automation and inspection technology in the field.

Responsibilities

- 1. Point-of-contact for planning and executing electronic component testing for all parts that flow through the Certified Authentic program
- 2. Run off-the-shelf equipment to perform AS6171 testing including general & detailed external visual inspection, X-ray, XRF, decapsulation, and electrical testing (LCR, DC, JTAG boundary-scan)
- 3. Integrate Chiplytics automation and inspection technology including the Micro-Inspection Platform (MIP) into testing and analysis workflows
- Build and run the lab: specify and commission equipment, establish benches/ESD
 controls, write work instructions and calibration/maintenance plans; contribute to facility
 layout and safe operations
- 5. Lead lab technicians and provide guidance on equipment setup, testing technique, and safe material handling and storage
- 6. Define requirements for test fixtures and more advanced electrical testing to be developed and/or performed by Chiplytics Engineering team
- 7. Coordinate outsourced testing for more specialized test cases (e.g. RF antenna testing)
- 8. Author test plans and reports: convert customer/industry requirements into clear procedures; produce defensible, standards-compliant reports with images, data, and pass/fail rationale.



- 9. Make final quality assessments on electronic components with input and approval from VP of Quality Assurance
- 10. Support technical reviews with suppliers and end-customers
- Drive process control: establish checklists, acceptance criteria, and control charts; partner with VP of QA to align with ISO 9001 and AS6171 quality management practices.
- 12. Manage lab scheduling, prioritize urgent lots, and ensure spares/consumables are stocked; track KPIs (turn-time, first-pass yield, rework rate).
- 13. Contribute to defining requirements and capabilities of the next generation of Chiplytics microelectronics inspection technology

Requirements

- 10+ years in electronic component testing with experience working with a team of lab technicians
- Hands-on experience running AS6171 inspection methods including general and detailed external visual inspection, X-ray, XRF, decapsulation, curve tracing, and boundary scan/JTAG
- Experience writing test plans and determining authenticity and integrity of microelectronics components
- Understanding of testing requirements and setup for a wide range of microelectronics components
- Well-versed at diagnosing, debugging, and maintaining lab equipment
- Familiarity writing code to perform testing and analysis (e.g. Python, Jupyter Notebook)
- Ability to work independently in a fast-paced startup; bias for action and ownership
- Eligibility for U.S. Government security clearance is a plus
- U.S. Citizen

How to Apply

Send an email to join@chiplytics.io with your resume and short paragraph about yourself and why you're interested in applying.